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SUN MICROSYSTEMS, INC., ("assignee"), a Delaware corporation having a place of business at 4150 Network Circle, Santa Clara, California 95054, certifies that to the best of assignee's knowledge and belief it is the assignee of the entire right, title, and interest in and to the above-referenced patent and represents that the undersigned is a representative authorized and empowered to sign on behalf of the assignee.

Assignee has reviewed the assignment documents that evidence the placement of title in the assignee, true and correct copies of which are attached hereto, and understands and believes that these assignment documents have been submitted for recordation in the U.S. Patent and Trademark Office.

Pursuant to 37 C.F.R. §§1.36 and 3.71, the assignee hereby revokes all powers of attorney previously given and appoints the following attorneys and/or agents in connection with the patent applications and patents identified in Appendix A, owned by SUN MICROSYSTEMS, INC., that are filed with the United States Patent and Trademark Office:

Andrew C. Chen	Reg. No. 43,544	Noreen A. Krall	Reg. No. 39,734
Bernice B. Chen	Reg. No. 42,403	Elaine K. Lee	Reg. No. 41,936
Arien C. Ferrell	Reg. No. 46,696	Sean P. Lewis	Reg. No. 42,798
Marilyn E. Glaubensklea	Reg. No. 35,521	Jeffrey L. Myers	Reg. No. 44,252
Anirima R. Gupta	Reg. No. 38,275	Joseph A. Twarowski	Reg. No. 42,191

*each of said attorneys being employed by Sun Microsystems; and*

Mark K. Brightwell	Reg. No. 47,446	Lawrence J. Merkel	Reg. No. 41,191
Kay A. Colapret	Reg. No. 52,759	Eric B. Meyertons	Reg. No. 34,876
Stephen J. Curran	Reg. No. 50,664	Neal E. Persky	Reg. No. 53,452
Mark R. DeLuca	Reg. No. 44,649	Liza Philip	Reg. No. 51,352
Heather L. Flanagan	Reg. No. 54,101	David W. Quimby	Reg. No. 39,338
Russell Henrichs	Reg. No. 50,354	Rory D. Rankin	Reg. No. 47,884
Erik A. Heter	Reg. No. 50,652	Gareth Sampson	Reg. No. 52,191
Jeffrey C. Hood	Reg. No. 35,198	Chris Thompson	Reg. No. 43,188
B. Noël Kivlin	Reg. No. 33,929	Mark S. Williams	Reg. No. 50,658
Robert C. Kowert	Reg. No. 39,255		

*each of said attorneys or agents being a member or an associate of the firm of Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C., as its attorney or agent for so long as they remain with such firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.*

Pursuant to 37 C.F.R. §3.71, the assignee hereby states that prosecution of the above-referenced patent application is to be conducted to the exclusion of the inventor(s).

Send all future correspondence to:

B. Noël Kivlin  
Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398  
(512) 853-8800

Assignee of Interest

SUN MICROSYSTEMS, INC.  
4150 Network Circle  
Santa Clara, California 95054

Dated: \_\_\_\_\_

5/18/04

By: \_\_\_\_\_



Andrew C. Chen  
Reg. No.: 43,544  
Patent Counsel

# APPENDIX A

6000-00400	P6632	F 8/29/03	10/652,192	Method and Apparatus for Vertex Splitting in a Graphics System	Chiu, Hu, Deering
6000-00500	P6276	F 5/30/03	10/449,926	History FIFO With Bypass	Smith
6000-00600	P5919	F 3/13/02	10/099,378	Flexible System Architecture with Common Interface for Multiple System Functions	Okin
6000-00700	P5918	F 3/12/02	10/099,716	System Architecture Providing Redundant Components to Improve Die Yields and System Reliability	
6000-00800	P6096	7/1/96	08/675,628	Computer Processing Architecture Having a Scalable Number of Processing Paths and Pipelines	Saulsbury, Parkin, Rice
6000-00900	P1723	F 3/8/01	09/802,108	Processor Architecture with Independent OS Resources	Parady